

Alberto

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- The video should contain a diagram of the architecture. Is difficult to follow the actions performed.
- Latency and jitter
 - + Try to compile the FTT-code using the Xenomai POSIX skin
 - + Try to use some RT network stack like
 - * RTnet (Paulo)
 - * Linux tc (traffic control) (Luis)
 - * PF_RING (Alberto)
 - * DPDK (Alberto)
 - + Luis had good experiences with proprietary RTOSs
 - + Maybe some parts of FTT can be implemented in hardware
- Uncontrolled growing
 - + The implementation of the FT mechanisms cannot be done on top of FTT
 - + Maybe the FTT separation of layers (core, management and interface) is not well suited for implementing FT mechanisms
 - + Pere and Alberto are starting to think how to put order on the code
- We should take decisions on how to proceed with the new project

Sinisa

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- Future of model checking is merging different techniques. For instance, a model checker that stops its operation and executes a simulator to get some value needed for the model.